FIG.1 Related Art

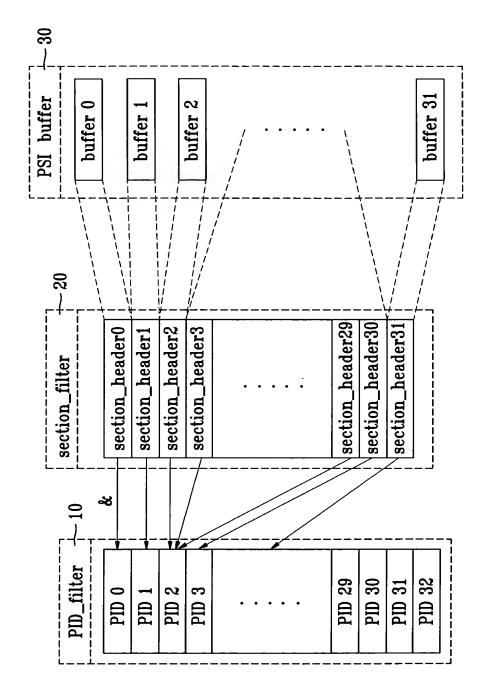


FIG.2

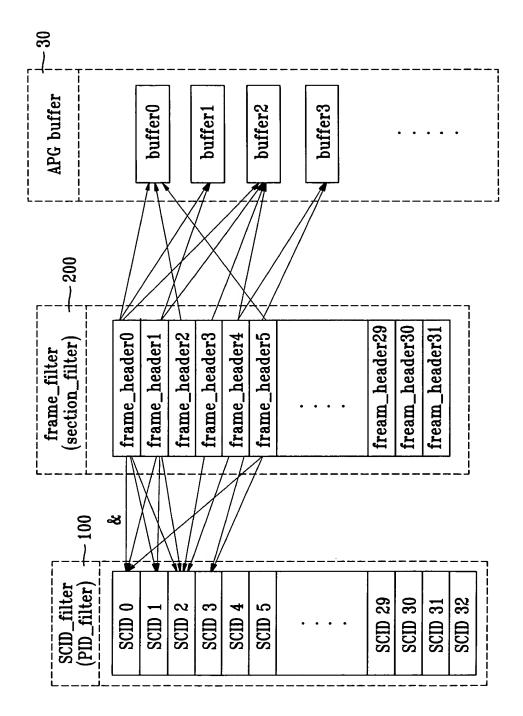


FIG.3

	ber	SCI	SCID_number	SCID_number
Irema_neader u	(3.01cs) ex) 0×0	(5 DICS) (x) = (x)	(3 DILS) (\mathbf{x})	(3 bits)
	SCID_number	SCID_number	SCID_number	SCID_number
frema_header 1	(5 bits)	(5 bits)	$(5 ext{ bits})$	mask_bits
	$ex)0 \times 1$	$ex)0\times2$	ex)None	(3 bits)
	SCID_number	SCID_number	SCID_number	SCID_number
frema_header 2	(5 bits)	(5 bits)	$(5 ext{ bits})$	mask_bits
	$ex)0 \times 0$	ex)None	ex)None	(3 bits)
	SCID_number	SCID_number	SCID_number	SCID_number
frema header 3	(5 bits)	(5 bits)	(5 bits)	mask_bits
	$ex)0\times 2$	ex)None	ex)None	(3 bits)
	SCID_number	SCID_number	SCID_number	SCID_number
frema_header 4	(5 bits)	$(5 ext{ bits})$	$(5 ext{ bits})$	mask_bits
	ex)0×2	$ex)0 \times 3$	ex)None	(3 bits)

frema_header 30	SCID_number (5 bits) $ex)0\times7$	SCID_number SCID_number (5 bits) (5 bits) (5 bits) ex)None	SCID_number (5 bits) ex)None	SCID_number mask_bits (3 bits)
frema_header 31	SCID_number (5 bits) ex) 0×14	SCID_number (5 bits)	SCID_number (5 bits) ex)0×1a	SCID_number mask_bits (3 bits)

FIG.4

	31	30	29		5	4	3	2	1	0
scid0	0	0	0		1	0	0	1	0	1
									•	
	31	30	29		5	4	3	2	1	0
scid1	0	0	0		0	0	0	0	1	1
				•						
				· ·						
	31	30	29		5	4	3	2	1	0
scid30	0	0	0	• • • •	0	1	0	0	1	0
	31	30	29		5	4	3	2	1	0
scid31	0	0	0		1	0	0	0	0	1

FIG.5

Frame_start_point (start address of buffer)	Frame_start_point (end address of buffer)	Matched SCID_number	Matched frame_header _number
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